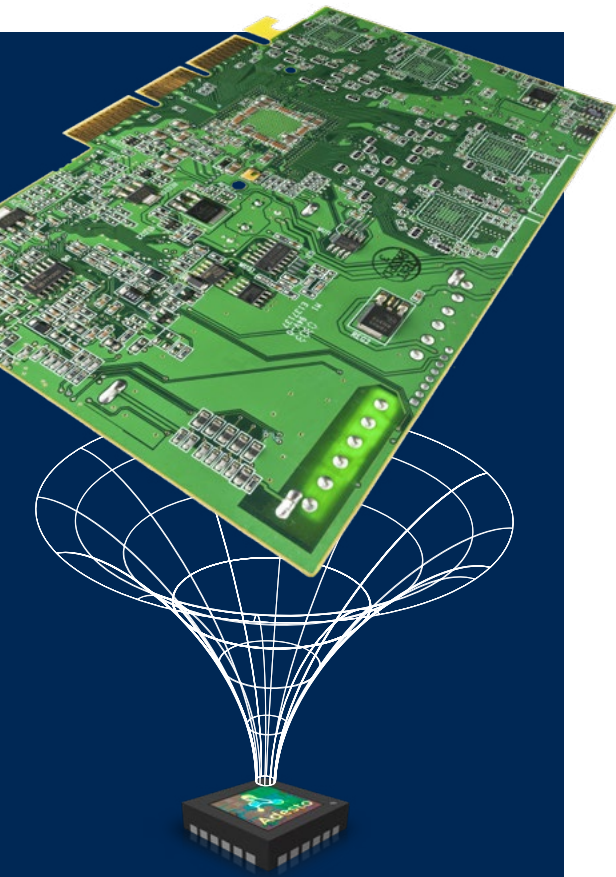
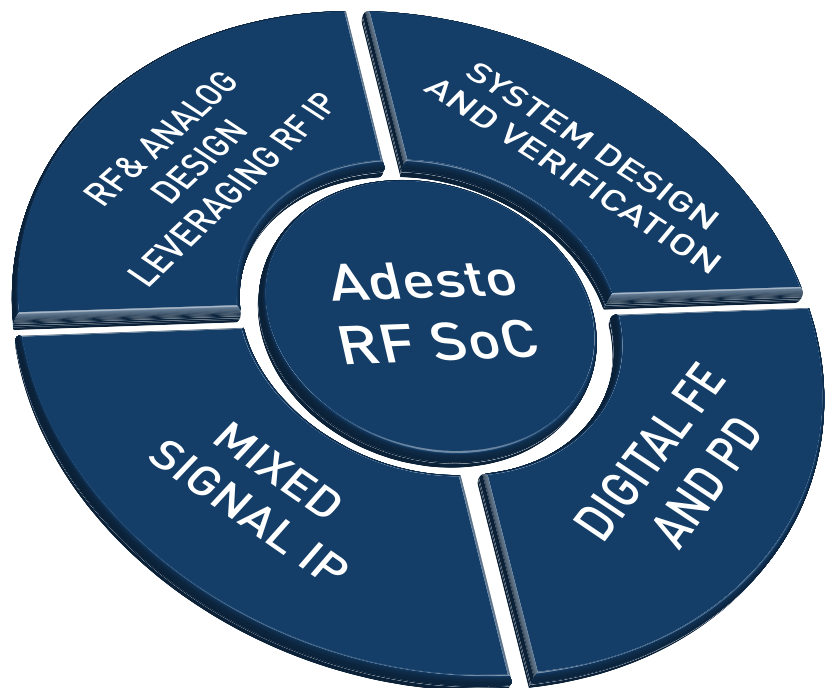
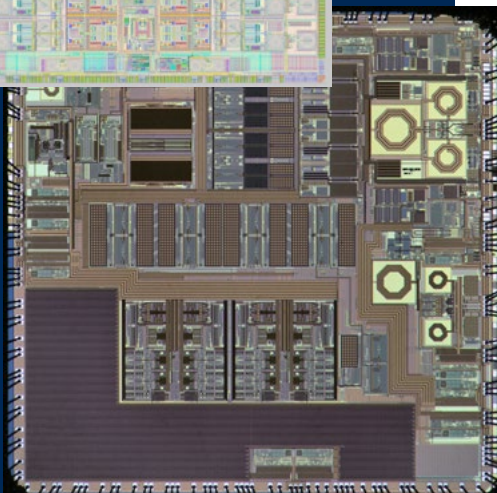
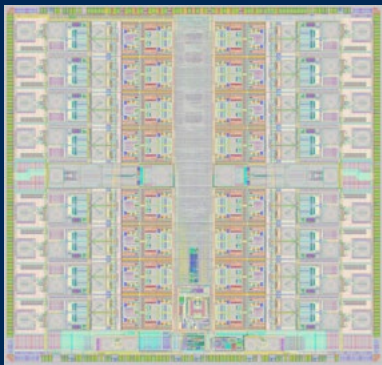


# RF Systems on Chip

Using standard CMOS technology

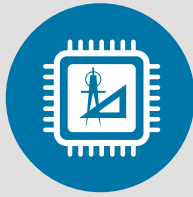


- Integrating proven RF and mixed signal IP to develop RF systems on chip (SoC) from VHF to millimeter wave
- Developing on-chip calibration techniques for digitally assisted analog & RF
- Leveraging CPU and digital accelerator IP, DSP including DPD, CPU, digital front end and physical design in integration expertise
- LNA, mixer, PA and PLL Synthesizer IP
- ADCs and DACs from Msps to Gps





**BOM analysis**



**IC Architects**

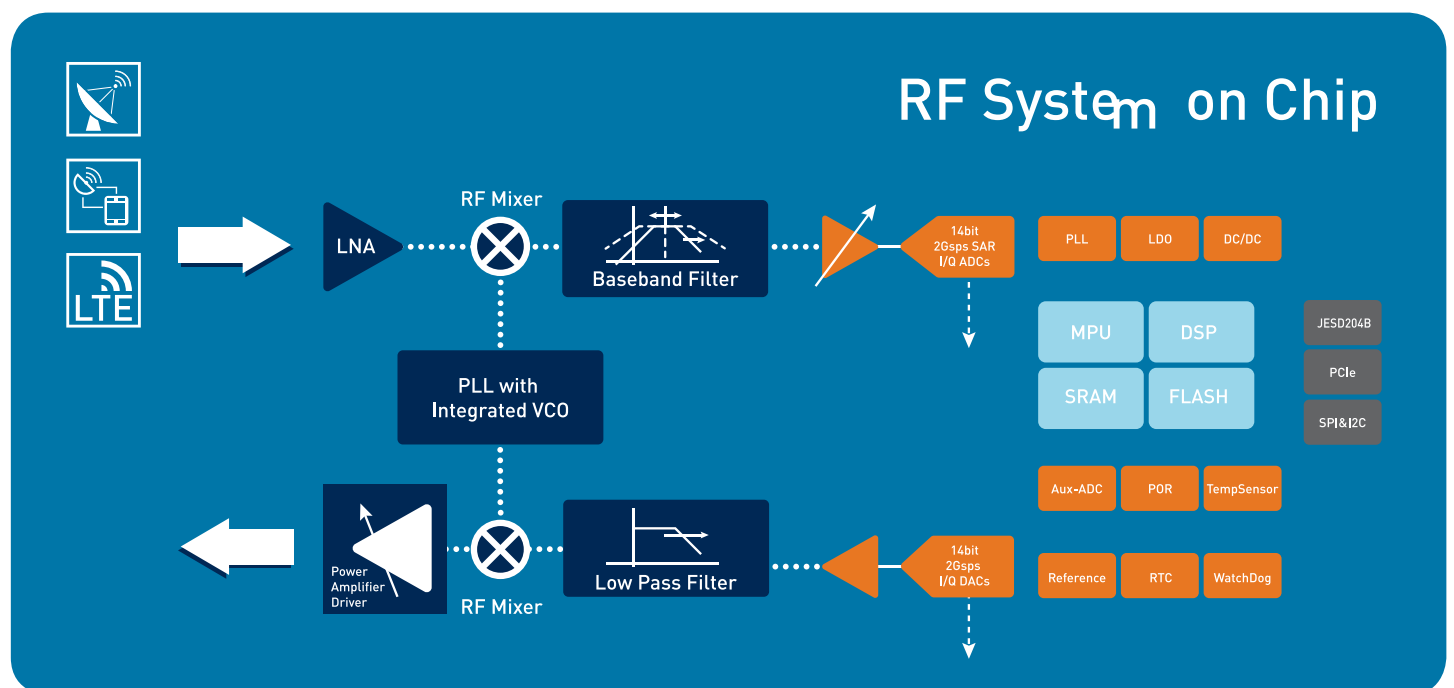


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Decades of RF design experience, coupled with silicon proven mixed-signal IP, mean fast, cost effective chip development and assured performance for your application

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- Up to 80% less BOM cost



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