USB Power Delivery 3.0 Controller with Integrated Current Sense Supports Qualcomm Quick Charge 4+

1 Description

The iW657P is a USB Power Delivery (USB PD) interface controller which handles the communication between a Power Adapter and Mobile Device (MD). The controller enables the mobile device to alter the \( V_{BUS} \) voltage from the default 5V and/or maximum current limitation (e.g. 3A) for higher power output or better efficiency. The iW657P supports USB PD 3.0, including Programmable Power Supply (PPS) support, while being backwards compatible with USB PD 2.0. The device also supports Qualcomm® Quick Charge™ 4+ technology. The iW657P enables the use of advanced USB Type-C™ connector technology with CC1/CC2 pins for MD attach/detach detection and \( V_{CONN} \) support for Electronically Marked Cable (EMC) rated current reading. The iW657P also integrates a high current charge-pump circuit to drive an external NFET \( V_{BUS} \) disconnect switch.

The iW657P resides on the secondary side of an AC/DC power supply and negotiates voltage and current settings with the primary-side controller, depending on the requests from the MD. The iW657P uses Dialog’s proprietary secondary-to-primary digital communication technology and when paired with one of Dialog’s primary-side RapidCharge™ controllers, such as the iW1781, iW1791 or iW1799, the iW657P eliminates the need for a discrete decoder on the primary side by using one optocoupler to transmit all necessary information for rapid charging to the primary side. This includes output voltage requests, output current limits, output voltage undershoot, output over-voltage, and fault and reset signals. The iW657P incorporates Dialog’s proven and reliable DLNK technology to communicate from the secondary to the primary and also has a built-in optocoupler LED driver to minimize the bill of material cost.

The power supply designed with the iW657P is fully protected. Using over-voltage protection on the D+/D-/CC1/CC2 pins, the iW657P helps to address soft short issues in cables and connectors caused by poor or loose connections between the cable connector and the socket, contamination in the USB connector, or a worn out cable. Additionally, proprietary short circuit protection on the \( V_{BUS} \) NFET ensures safe operation in the event of a short on the output, while the SD pin can be used with an external NTC resistor for protection from over-temperature faults. The iW657P also integrates a secondary-side current sensing circuit that provides additional over-current protection for the power adapter.

2 Features

- USB-IF PD certified: USB PD 3.0 + Programmable Power Supply (PPS)
  - Maximum power limit in PPS to minimize power adapter size
  - Integrated CC transmitter/receiver supports BMC communication
  - Backwards-compatible with USB PD 2.0
- Supports Qualcomm Quick Charge 4+ Technology
- Supports up to 7 Power Data Objects (PDOs)
- Supports wide output voltage range from 3.3 to 21V
- Compatible with USB Type-C specification Rev 1.2 for MD attach/detach detection and \( V_{CONN} \) support for smart cables
- Integrated charge pump supports single NFET \( V_{BUS} \) switch
- 9-bit ADC provides accurate current (optional) and voltage sensing
- Proprietary \( V_{BUS} \) NFET protection to protect \( V_{BUS} \) switch from damage due to an output short circuit
- Proprietary secondary-to-primary digital communication eliminates discrete decoders on the primary side and simplifies system designs
- D+/D-/CC1/CC2 over-voltage protection (OVP) address soft short issues in the output cables and connectors
- Optional secondary-side current sensing circuit provides additional protection for over-load fault
- Uses external NTC for power adapter temperature sensing
- Programmable active fast discharge from a high voltage \( V_{BUS} \) level to 5V at MD unplug or upon request with built-in switch or external switch
- Intelligent circuits helps achieve < 20mW system no-load power at 5V steady-state operation.
- 14-pin 4x3mm TDFN package

3 Applications

- Rapid-charging AC/DC adapters for smart phones, tablets and other portable devices

Qualcomm® Quick Charge™ is a product of Qualcomm Technologies, Inc.
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Figure 3.1: iW657P Typical Application Circuit with Secondary-Side Current Sensing
(with iW1791 as the Primary-Side Controller and the iW676 as the Synchronous Rectifier Controller)

Figure 3.2: iW657P Typical Application Circuit without Secondary-Side Current Sensing
(with iW1791 as the Primary-Side Controller and the iW676 as the Synchronous Rectifier Controller)
Figure 3.3: iW657P Typical Application Circuit with Secondary-Side Current Sensing
(with iW1799 as the Primary-Side Controller and the iW676 as the Synchronous Rectifier Controller)
iW657P

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4 Pinout Description

<table>
<thead>
<tr>
<th>Pin Number DFN-14</th>
<th>Pin Name</th>
<th>Type</th>
<th>Pin Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>DIS</td>
<td>Analog Output</td>
<td>Discharging circuit. Used for fast discharging of output capacitor.</td>
</tr>
<tr>
<td>2</td>
<td>DRV</td>
<td>Analog Output</td>
<td>External circuit drive. Can be used to drive optocoupler LED with automatic current limiting for transmitting signals to primary side.</td>
</tr>
<tr>
<td>3</td>
<td>V_CC</td>
<td>Power Supply</td>
<td>IC power supply.</td>
</tr>
<tr>
<td>4</td>
<td>DET</td>
<td>Analog Input</td>
<td>AC unplug detect.</td>
</tr>
<tr>
<td>5</td>
<td>V_BUS</td>
<td>Analog Input/Output</td>
<td>Monitor V_BUS voltage after N-FET switch.</td>
</tr>
<tr>
<td>6</td>
<td>V_BUS_G</td>
<td>Analog Input/Output</td>
<td>Connect to external N-FET gate pin for gate-source voltage control.</td>
</tr>
<tr>
<td>7</td>
<td>CC2</td>
<td>Analog Input/Output</td>
<td>Configuration Channel 2.</td>
</tr>
<tr>
<td>8</td>
<td>CC1</td>
<td>Analog Input/Output</td>
<td>Configuration Channel 1.</td>
</tr>
<tr>
<td>9</td>
<td>IS-</td>
<td>Analog Input</td>
<td>Output current sensing terminal - (for current sensing resistor).</td>
</tr>
<tr>
<td>10</td>
<td>IS+</td>
<td>Analog Input</td>
<td>Output current sensing terminal + (for current sensing resistor).</td>
</tr>
<tr>
<td>11</td>
<td>SD</td>
<td>Analog Input/Output</td>
<td>Connect to an external NTC resistor to measure the power adapter temperature.</td>
</tr>
<tr>
<td>12</td>
<td>D-</td>
<td>Analog Input/Output</td>
<td>USB D- signal.</td>
</tr>
<tr>
<td>13</td>
<td>GND</td>
<td>Ground</td>
<td>Ground.</td>
</tr>
<tr>
<td>14</td>
<td>D+</td>
<td>Analog Input/Output</td>
<td>USB D+ signal.</td>
</tr>
</tbody>
</table>

Figure 4.1: 14-Lead TDFN 4x3mm Package
(Top View, Transparent)
5 Absolute Maximum Ratings

Absolute maximum ratings are the parameter values or ranges which can cause permanent damage if exceeded.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Value</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{CC}$ voltage</td>
<td>$V_{CC}$</td>
<td>-0.3 to 30</td>
<td>V</td>
</tr>
<tr>
<td>DIS voltage</td>
<td>$V_{DIS}$</td>
<td>-0.3 to 30</td>
<td>V</td>
</tr>
<tr>
<td>DRV voltage</td>
<td>$V_{DRV}$</td>
<td>-0.3 to 30</td>
<td>V</td>
</tr>
<tr>
<td>$V_{SD}$ voltage</td>
<td>$V_{SD}$</td>
<td>-0.3 to 7</td>
<td>V</td>
</tr>
<tr>
<td>D- voltage</td>
<td>$V_{D-}$</td>
<td>-0.3 to 7</td>
<td>V</td>
</tr>
<tr>
<td>D+ voltage</td>
<td>$V_{D+}$</td>
<td>-0.3 to 7</td>
<td>V</td>
</tr>
<tr>
<td>CC1 voltage</td>
<td>$V_{CC1}$</td>
<td>-0.3 to 30</td>
<td>V</td>
</tr>
<tr>
<td>CC2 voltage</td>
<td>$V_{CC2}$</td>
<td>-0.3 to 30</td>
<td>V</td>
</tr>
<tr>
<td>$V_{BUS}$ voltage (in $V_{BUS} &lt; 10mA$)</td>
<td>$V_{BUS}$</td>
<td>-0.7 to 30</td>
<td>V</td>
</tr>
<tr>
<td>$V_{BUS_G}$ voltage</td>
<td>$V_{BUS_G}$</td>
<td>-0.7 to 35</td>
<td>V</td>
</tr>
<tr>
<td>DET voltage</td>
<td>$V_{DET}$</td>
<td>-0.7 to 30</td>
<td>V</td>
</tr>
<tr>
<td>Peak current at DIS pin ($V_{DIS} = 12V$)</td>
<td>$I_{DIS}$</td>
<td>600</td>
<td>mA</td>
</tr>
<tr>
<td>IS+ voltage</td>
<td>$V_{IS+}$</td>
<td>-0.3 to 7</td>
<td>V</td>
</tr>
<tr>
<td>IS- voltage</td>
<td>$V_{IS-}$</td>
<td>-0.3 to 7</td>
<td>V</td>
</tr>
<tr>
<td>Maximum junction temperature</td>
<td>$T_{JMAX}$</td>
<td>150</td>
<td>°C</td>
</tr>
<tr>
<td>Operating junction temperature</td>
<td>$T_{JOPT}$</td>
<td>-40 to 150</td>
<td>°C</td>
</tr>
<tr>
<td>Storage temperature</td>
<td>$T_{STG}$</td>
<td>-65 to 150</td>
<td>°C</td>
</tr>
<tr>
<td>ESD rating per JEDEC JESD22-A114 (D+, D-, CC1, CC2)</td>
<td>±8,000</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>ESD rating per JEDEC JESD22-A114 (all other pins)</td>
<td>±2,000</td>
<td>V</td>
<td></td>
</tr>
</tbody>
</table>

Notes:

Note 1. Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, so functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specification are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.
6 Physical Dimensions

Figure 6.1: 14-Lead TDFN 4x3mm Package

7 Part Number Code Description

iW657P-XX-YYY

<table>
<thead>
<tr>
<th>Base Part Number</th>
<th>Travel Adapter Maximum Output Power (W)</th>
<th>Product Option</th>
</tr>
</thead>
</table>

1. All dimensions are in mm, unless otherwise specified.
2. Dimensions apply to the exposed lead as well as the terminals. Exposed lead dimension shall not exceed 0.100 mm.
3. Part number shall not exceed 23 characters.
4. PACKAGE LENGTH / PACKAGE WIDTH TO BE CONFORMED TO SPC:2157.
5. REFER NC - 30-229.
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Contacting Dialog Semiconductor

United Kingdom (Headquarters)
Dialog Semiconductor (UK) LTD
Phone: +44 1793 757700

Germany
Dialog Semiconductor GmbH
Phone: +49 7021 805-0

The Netherlands
Dialog Semiconductor B.V.
Phone: +31 73 640 8922
Email
info_pcbg@diasemi.com

North America
Dialog Semiconductor Inc.
Phone: +1 408 845 8500

Japan
Dialog Semiconductor K. K.
Phone: +81 3 5769 5100

Taiwan
Dialog Semiconductor Taiwan
Phone: +886 281 786 222
Web site:
www.dialog-semiconductor.com

Hong Kong
Dialog Semiconductor Hong Kong
Phone: +852 2607 4271

Korea
Dialog Semiconductor Korea
Phone: +82 2 3469 8200

China (Shenzhen)
Dialog Semiconductor China
Phone: +86 755 2981 3669

China (Shanghai)
Dialog Semiconductor China
Phone: +86 21 5424 9058

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