Abstract

Guidelines for designing printed antennas for 2.4 GHz Bluetooth® low energy applications on single-layer and multi-layer PCB stack-ups.
Designing Printed Antennas for Bluetooth® Low Energy

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1 Terms and Definitions

balun balanced/unbalanced (transformer)
BLE Bluetooth low energy
DNP Do Not Populate
GND Ground (plane)
IC Integrated Circuit
IFA Inverted-F Antenna
PCB Printed Circuit Board
XTAL (quartz) crystal
ZOR Zeroth-Order Resonance (antenna)

2 References

N/A

3 Introduction

This application note contains guidelines for implementing printed PCB antennas for Bluetooth® low energy applications operating in the 2.4 GHz frequency band.

Basic rules are given on what to avoid when starting with an antenna design.

For multi-layer PCBs several antenna options are given, depending on the available PCB space. There is a trade-off between antenna size and radiation performance. Full size antennas are more broadband and easier to match since the impedance is closer to 50 Ω.

For single-layer PCBs the main challenge is providing a proper ground (GND) plane for the antenna. When a lower quality substrate material is used to reduce cost, this will also have an impact on the radiation efficiency of the antenna.
4 RF Layout Design

Guidelines to consider for general RF layout work.

4.1 Radio IC

- Active components operating at high frequency should have as compact a layout as possible. This will prevent cross-coupling between lines and parasitics, which will have a negative effect on the operating parameters.
- Always provide a solid grounding to the radio IC. Use as many vias as possible to create a solid GND under the IC itself and connect it to inner and bottom GND layers.
- Place the XTAL used as reference for the RF carrier (16 MHz for the DA1458x) as close as possible to the IC. This minimizes any additional capacitive load on the input pins and reduces the chance of crosstalk and interference with other signals on the board.

Figure 1: DA1458x_CB_PXI_WLCSP Board Layout Snapshot
4.2 RF Transmission Lines, Filters, and Matching Networks

- Place radio matching components and any RF filters as close as possible to the radio IC.
  - Make sure the component’s GNDs are connected to the same GND plane (left side of the transmission line above).
- Minimize the transmission line length between radio IC and antenna.
  - The characteristic impedance of the transmission line should match the required radio impedance (50 Ω for the DA1458x).
  - Most current PCB layout tools have the possibility to calculate the characteristic impedance of the transmission line based on the stack-up and PCB material.
- Place antenna matching components as close as possible to the feeding point of the antenna.
  - Make sure the component’s GNDs are connected to the same GND plane (left side of the transmission line above).

4.3 Antennas, General Considerations

Consider the following remarks before starting with an antenna design:

- Do not place metal layers below the antenna itself. The antenna footprint must be kept free of metal.
- Do not place metal screws, radiators, piezo buzzers, batteries, etc. in the proximity of the antenna.
  - As a rule of thumb, at least 5 mm should be allowed around the antenna footprint, both horizontally on the PCB and vertically around the PCB footprint.
- Do not use metal enclosures for products with antennas. This will prevent the antenna from radiating and performing as intended.
5 Antennas on Multi-Layer Substrates

On a multi-layer PCB stack-up it is relatively easy to construct a good reference GND plane for the antenna. This is especially important because the most common antenna choice is the Inverted-F Antenna (IFA).

It is very important to understand that IFAs operate in conjunction with a GND plane. There is a minimum required size of the GND plane for efficient antenna operation. This parameter has been specifically indicated for all designs below.

Depending on the available space on the PCB one can choose between a full size IFA and a folded/shrunked version.

The full size IFA has more broadband frequency characteristics and better radiation efficiency. The folded versions can reduce the space requirements by half and still achieve 100 MHz to 150 MHz bandwidth, which is enough to cover the Bluetooth low energy 2.4 GHz band (at -10 dB return loss).

The achievable radiation efficiency is between -1.5 dB and -3 dB. Efficiency is affected by the PCB material, therefore it is recommended that FR4 (low loss) substrate material is used.

The antennas in the following sections were designed and measured using:

- FR4 PCB substrate material
- 2 or more metal (copper) layers

The dimensions given below are based on laboratory measurements and are optimised for the given stack-up and board size.

Variations in board thickness or substrate material will have an effect on the antenna matching. Also, when the antenna is in close proximity of a plastic enclosure the optimum dimensions and matching components may be different.
5.1 Full Size Printed IFA, 1 mm Substrate

The red outline indicates the antenna footprint, i.e. required allocation of PCB space. The antenna footprint is available on request in DXF format.

Legend (see Figure 3):

a. Clearance between antenna arm and GND plane left.
   
   i. For narrow PCBs (a) and (b) will coincide with board edges.
   
   ii. The two GND pieces left and right of the antenna are NOT required for correct antenna operation.

b. Clearance between antenna arm and GND plane right.

   i. The feeding line (from indicated matching components) is implemented on top layer only.

   ii. The antenna is implemented on top and bottom layers and stitched together using vias.

c. Clearance between antenna arm and GND plane below.

d. Minimum GND plane size required for correct operation of the antenna.

e. Antenna width.

   i. The antenna trace width (0.6 mm).

   ii. Feed point position.
5.1.1 Matching Network

The matching is subject to change depending on substrate type or thickness and enclosure material (type and proximity to PCB).

![Matching Components, Full Size IFA](image)

- C1: 2.4 pF, 0402, GRM15 series, Murata
- L1: 1.8 nH, 0402, LQP series, Murata
- C2: 2.7 pF, 0402, GRM15 series, Murata
- In addition to the matching network, a 3.3 nH or 3.9 nH coil (depending on the DA1458x package) is required close to the RFIOp pin.

5.1.2 Measured Return Loss

![Measured S11, Full Size IFA](image)
5.1.3 Measured Radiation Pattern

Figure 6: Radiation Pattern, PCB Horizontal, Full Size IFA

Figure 7: Radiation Pattern, PCB Vertical, Full Size IFA
5.2 Reduced Size Printed IFA, 1 mm Substrate, Variant 1

The red outline indicates the antenna footprint, i.e. required allocation of PCB space. The antenna is implemented on the TOP layer only. The antenna footprint is available on request in DXF format.

Legend (see Figure 4):

a. Clearance between antenna and GND plane left.
b. Clearance between antenna and GND plane right.
   i. For narrow PCBs (a) and (b) will coincide with board edges.
   ii. The two GND pieces left and right of the antenna are NOT required for correct antenna operation.
c. Clearance between antenna arm and GND plane below.
d. Minimum GND plane size required for correct operation of the antenna.
e. Antenna width.
f. Antenna main arm.
   i. Width: 0.8 mm (30 mil).
   ii. Length: 24.4 mm (961 mil).
g. Antenna meander arm.
   i. Implemented on the TOP layer only.
   ii. Width: 0.153 mm (6 mil).
   iii. Length: 21.22 mm (835.5 mil).
5.2.1 Matching Network

The matching is subject to change depending on substrate type or thickness and enclosure material (type and proximity to PCB).

![Matching Components Diagram]

Figure 9: Matching Components, Reduced Size IFA, Variant 1

- C1: DNP
- C2: 10 pF, 0402, GRM15 series, Murata
- L1: 10 nH, 0402, LQP series, Murata
- In addition to the matching network, a 3.3 nH or 3.9 nH coil (depending on the DA1458x package) is required close to the RFIOp pin.

5.2.2 Measured Return Loss

![Measured S11 Diagram]

Figure 10: Measured S11, Reduced Size IFA, Variant 1
5.2.3 Measured Radiation Pattern

Figure 11: Radiation Pattern, PCB Horizontal, Reduced Size IFA, Variant 1

Figure 12: Radiation Pattern, PCB Vertical, Reduced Size IFA, Variant 1
5.3 Reduced Size Printed IFA, 1 mm Substrate, Variant 2

The red outline indicates the antenna footprint, i.e. required allocation of PCB space. The antenna is implemented on the TOP layer only. The antenna footprint is available on request in DXF format.

Legend (see Figure 13):

- a. Antenna footprint width.
- b. Antenna footprint height.
- c. Minimum GND plane size required for correct operation of the antenna.
5.3.1 Matching Network

The matching is subject to change depending on substrate type or thickness and enclosure material (type and proximity to PCB).

![Matching Components, Reduced Size IFA, Variant 2](image)

- L1: 3.9 nH, 0402, LQP series, Murata
- L2: 5.1 nH, 0402, LQP series, Murata
- C1, DNP
- In addition to the matching network, a 3.3 nH or 3.9 nH coil (depending on the DA1458x package) is required close to the RFIOp pin.

5.3.2 Measured Return Loss

![Measured S11, Reduced Size IFA, Variant 2](image)
5.3.3 Measured Radiation Pattern

Figure 16: Radiation Pattern, PCB Horizontal, Reduced Size IFA, Variant 2

Figure 17: Radiation Pattern, PCB Vertical, Reduced Size IFA, Variant 2
5.4 ZOR Antenna, 1.6 mm Substrate

This variant of the Zeroth Order Resonance (ZOR) antenna is designed for a substrate thickness of 1.6 mm and requires a minimum of two layers. The TOP and BOTTOM layers are used below.

The red outline indicates the antenna footprint, i.e. required allocation of PCB space. The antenna footprint is available on request in DXF format.

Legend (see Figure 18):

a. Clearance between antenna and GND plane right.
   i. 4.0 mm (157.5 mil).
   ii. The GND on the right of the antenna is NOT required for correct antenna operation.

b. Clearance between antenna and GND plane below.
   i. 1.0 mm (39 mil).

c. Antenna length
   i. 9.0 mm (354 mil).

d. Antenna width
   i. 5.8 mm (228 mil).
5.4.1 Matching Network

The matching is subject to change depending on substrate type or thickness and enclosure material (type and proximity to PCB).

![Matching Components Diagram]

- L1: 1.5 nH, 0402, LQP series, Murata
- C1, 2.2 pF, 0402, GRM series, Murata
- In addition to the matching network, a 3.3 nH or 3.9 nH coil (depending on the DA1458x package) is required close to the RFIOp pin.

5.4.2 Measured Return Loss

![Measured S11 Diagram]

Figure 19: Matching Components, ZOR Antenna, 1.6 mm Substrate

Figure 20: Measured S11, ZOR Antenna, 1.6 mm Substrate
5.4.3 Measured Radiation Pattern

Figure 21: Radiation Pattern, DUT Horizontal, ZOR Antenna, 1.6 mm Substrate

Figure 22: Radiation Pattern, DUT Vertical, ZOR Antenna, 1.6 mm Substrate
5.5 ZOR Antenna, 1.0 mm Substrate

This variant of the Zeroth Order Resonance (ZOR) antenna is designed for a substrate thickness of 1.0 mm and requires a minimum of two layers. The TOP and BOTTOM layers are used below.

The red outline indicates the antenna footprint, i.e. required allocation of PCB space. The antenna footprint is available on request in DXF format.

Legend (see Figure 23):

a. Clearance between antenna and GND plane right.
   i. 4.0 mm (157.5 mil).
   ii. The GND on the right of the antenna is NOT required for correct antenna operation.

b. Clearance between antenna and GND plane below.
   i. 1.0 mm (39 mil).

c. Antenna length
   i. 9.0 mm (354 mil).

d. Antenna width
   i. 5.8 mm (228 mil).
5.5.1 Matching Network

The matching is subject to change depending on substrate type or thickness and enclosure material (type and proximity to PCB).

![Matching Network Diagram](image)

- **L1**: 3.6 nH, 0402, LQP series, Murata
- **C1**: 1.1 pF, 0402, GRM series, Murata
- In addition to the matching network, a 3.3 nH or 3.9 nH coil (depending on the DA1458x package) is required close to the RFIOp pin.

5.5.2 Measured Return Loss

![Measured Return Loss Graph](image)

**Figure 24: Matching Components, ZOR Antenna, 1.0 mm Substrate**

**Figure 25: Measured S11, ZOR Antenna, 1.0 mm Substrate**
5.5.3 Measured Radiation Pattern

Figure 26: Radiation Pattern, DUT Horizontal, ZOR Antenna, 1.0 mm Substrate

Figure 27: Radiation Pattern, DUT Vertical, ZOR Antenna, 1.0 mm Substrate
6 Antennas on Single Layer Substrates

Single layer substrates require a different approach with regard to antennas. Due to limited space and possibility to construct a continuous GND plane, printed dipoles are often used instead of IFAs. The dipole requires a balun when the radio input is single ended and therefore has additional costs in components.

Due to the single ended input of the DA1458x the IFA is still the best choice, but care must be taken that a proper GND plane is available. There are two main aspects to take care of:

- Continuous GND plane at the antenna (see section 6.2).
- Minimum GND plane size for correct antenna operation.

Because the antenna is implemented on one metal layer, the size of the antenna is slightly larger than the equivalent multi-layer design.

Single layer substrates are usually used to reduce costs and have a lower RF performance due to higher dielectric losses. This has a direct impact on the radiation efficiency. A typical IFA on an FR1 substrate can have a radiation efficiency between -2.5 dB and -5 dB.

6.1 Carbon Layers

Single layer PCBs are often used for low cost remote controls. It is common to use a carbon layer for the keyboard matrix or to have carbon bridges to route signals. The following rules must be applied to ensure maximum RF performance:

- Do not route RF signals in carbon.
- Do not cross RF transmission lines with carbon bridges.
  - An exception can be made when the carbon layer is on the other side of the PCB (i.e. copper on TOP and carbon on BOTTOM).
- Do not place carbon under the antenna.
  - The carbon is a conductive high loss material and will act as an absorber, thereby drastically reducing the radiation efficiency and the range of the antenna.
6.2  Single Layer Printed IFA, 1 mm Substrate

The dimensions above are given for a typical FR1 PCB substrate, 1 mm thick. The antenna length is adjusted for resonance, including a 1 mm plastic enclosure placed in contact with the PCB antenna.

The red outline indicates the antenna footprint, i.e. required allocation of PCB space. The antenna footprint is available on request in DXF format.

Legend (see Figure 28):

a. Clearance between antenna arm and GND plane right.
b. Antenna width.
c. Antenna height.
d. Clearance between the antenna arm and GND plane below.
e. Minimum GND plane size required for correct operation of the antenna.
f. Antenna trace width.
g. Feed point position.
h. 0 Ω 0603 resistor used to connect the two sides of the GND planes.
   i. When no DC connection is required, a 10 pF capacitor can also be used.
   ii. This component is essential for the correct operation of the antenna.
6.2.1 Matching Network

The matching is subject to change depending on substrate type or thickness and enclosure material (type and proximity to PCB).

![Matching Components, Single Layer IFA](image)

- L1: 7.5 nH, 0402, LQP series, Murata
- C1: 1.0 pF, 0402, GRM15 series, Murata
- L2: DNP

In addition to the matching network, a 3.3 nH or 3.9 nH coil (depending on the DA1458x package) is required close to the RFIOp pin.

6.2.2 Measured Return Loss

![Measured S11, Single Layer IFA](image)
6.2.3 Measured Radiation Pattern

Figure 31: Radiation Pattern, PCB Horizontal, Single Layer IFA

Figure 32: Radiation Pattern, PCB Vertical, Single Layer IFA
7 Conclusions

Antennas play an important role in the overall performance of any radio based equipment. The guidelines in this document will enable customers to integrate printed low cost antennas in their Bluetooth low energy products with minimum effort and RF knowledge.

The antennas were designed, tested and measured at Dialog Semiconductor and are currently used in many of our customers’ products.
8 Revision History

<table>
<thead>
<tr>
<th>Revision</th>
<th>Date</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.0</td>
<td>29-Aug-2014</td>
<td>Initial version.</td>
</tr>
<tr>
<td>2.0</td>
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<td>Added ZOR antenna, 1.0 mm substrate (section 5.5).</td>
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<td>2.2</td>
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<td>Generalizing the document for DA1458x family.</td>
</tr>
</tbody>
</table>
Designing Printed Antennas for Bluetooth® Low Energy

Status Definitions

<table>
<thead>
<tr>
<th>Status</th>
<th>Definition</th>
</tr>
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<tbody>
<tr>
<td>DRAFT</td>
<td>The content of this document is under review and subject to formal approval, which may result in modifications or additions.</td>
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</tbody>
</table>

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